

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	10	"061695"	US-PGPUB	OR	OFF	2004/12/07 14:25
S2	9	(santhanam-sribalan vonkaenel kruckemyer).in.	US-PGPUB	OR	OFF	2004/12/07 14:29
S3	6	(santhanam-sribalan vonkaenel kruckemyer).in.	USPAT	OR	OFF	2004/12/07 14:30
S4	4996	clock near gat\$3	USPAT	OR	OFF	2004/12/07 14:30
S5	39	S4 with tree	USPAT	OR	OFF	2004/12/07 14:42
S6	169	S4 near4 (level stage)	USPAT	OR	OFF	2004/12/07 14:43
S7	10	S6.ab.	USPAT	OR	OFF	2004/12/07 14:42
S8	63	(level\$1 stage\$1) adj2 S4	USPAT	OR	OFF	2004/12/07 14:52
S9	10659	(fine coarse) adj (grain granularity)	USPAT	OR	OFF	2004/12/07 15:11
S10	1	S4 with S9	USPAT	OR	OFF	2004/12/07 14:46
S11	1	S4 same S9	USPAT	OR	OFF	2004/12/07 14:47
S12	11	S4 same (global with local)	USPAT	OR	OFF	2004/12/07 14:47
S13	0	("6583648").URPN.	USPAT	OR	OFF	2004/12/07 14:50
S14	3	("6020774"   "6313683"   "6452435").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/07 14:51
S15	4	(level\$1 stage\$1) adj "clock gating"	USPAT	OR	OFF	2004/12/07 15:06
S16	11	(level\$1 stage\$1) adj "clock gating"	US-PGPUB	OR	OFF	2004/12/07 15:04
S17	3	(multilevel multistage) with "clock gating"	US-PGPUB	OR	OFF	2004/12/07 15:06
S18	0	(multilevel multistage) with "clock gating"	USPAT	OR	OFF	2004/12/07 15:06
S19	0	(hierarchy hierarchical) near6 "clock gating"	USPAT	OR	OFF	2004/12/07 15:07
S20	4	(hierarchy hierarchical) same "clock gating"	USPAT	OR	OFF	2004/12/07 15:07
S22	1	(nesting nested) same "clock gating"	USPAT	OR	OFF	2004/12/07 15:10
S23	2366	(fine coarse) adj (grain granularity)	US-PGPUB	OR	OFF	2004/12/07 15:11
S24	5	S23 with "clock gating"	US-PGPUB	OR	OFF	2004/12/07 15:14
S25	2	(local near8 global) with "clock gating"	US-PGPUB	OR	OFF	2004/12/07 15:14
S26	0	(local near8 global) with "clock gating"	EPO; JPO; IBM_TDB	OR	OFF	2004/12/07 15:14
S27	0	(local near8 global) same "clock gating"	EPO; JPO; IBM_TDB	OR	OFF	2004/12/07 15:14
S28	0	(coarse fine) adj (grain granularity) same "clock gating"	EPO; JPO; IBM_TDB	OR	OFF	2004/12/07 15:15
S29	6	(level\$1 stage\$1) near2 "clock gating"	EPO; JPO; IBM_TDB	OR	OFF	2004/12/07 15:15
S30	0	further near gat\$3	USPAT	OR	OFF	2004/12/07 15:26
S31	2789	additional near gat\$3	USPAT	OR	OFF	2004/12/07 15:25
S32	11	S31 with tree	USPAT	OR	OFF	2004/12/07 15:26
S33	0	"further" near gat\$3	USPAT	OR	OFF	2004/12/07 15:26
S37	753	clock adj2 tree	USPAT	OR	OFF	2004/12/07 16:34
S38	9	local adj S37	USPAT	OR	OFF	2004/12/07 16:57
S39	1496	subtree	USPAT	OR	ON	2004/12/20 14:57
S40	4	gat\$3 near4 S39	USPAT	OR	OFF	2004/12/07 16:45
S41	629	sub-tree	USPAT	OR	ON	2004/12/07 16:48
S42	6	gat\$3 near4 S41	USPAT	OR	ON	2004/12/07 16:48
S43	361	gat\$3 near4 (subcircuit sub-circuit)	USPAT	OR	ON	2004/12/07 16:49
S44	47	gat\$3 adj (subcircuit sub-circuit)	USPAT	OR	ON	2004/12/07 16:51
S45	3	S44.ab.	USPAT	OR	ON	2004/12/07 16:49
S46	14	S43.ab.	USPAT	OR	ON	2004/12/07 16:51

S47	81	S43.clm.	USPAT	OR	ON	2004/12/07 16:51
S48	14	global adj S37	USPAT	OR	OFF	2004/12/07 16:57
S50	2309	clock adj2 distribution	USPAT	OR	OFF	2004/12/17 11:44
S51	659	clock adj tree	USPAT	OR	OFF	2004/12/17 12:00
S52	12	(S50 S51) near2 power adj (consumed consumption dissipat\$3)	USPAT	OR	OFF	2004/12/17 11:46
S53	756	clock adj2 tree	USPAT	OR	OFF	2004/12/17 12:01
S54	54	"within" adj (S50 S53)	USPAT	OR	OFF	2004/12/17 12:40
S55	2	S54 with switch\$3	USPAT	OR	OFF	2004/12/17 12:10
S56	3	(reduc\$3 minimiz\$3) adj3 switch\$3 with (S50 S53)	USPAT	OR	OFF	2004/12/17 12:12
S57	580	sublevel sub-level	USPAT	OR	OFF	2004/12/17 12:16
S58	0	S57 with (S50 S53)	USPAT	OR	OFF	2004/12/17 12:16
S59	1704	subtree sub-tree	USPAT	OR	OFF	2004/12/17 12:16
S60	0	S59 with enable near (clock clk)	USPAT	OR	OFF	2004/12/17 12:16
S61	32	S59 with enabl\$3	USPAT	OR	OFF	2004/12/17 12:19
S62	692	(hierarchy hierarchical) near4 enabl\$3	USPAT	OR	OFF	2004/12/17 12:19
S63	28	(hierarchy hierarchical) with enabl\$3 near4 (clock clk)	USPAT	OR	OFF	2004/12/17 12:21
S64	23	(hierarchy hierarchical) with gating	USPAT	OR	OFF	2004/12/17 12:21
S65	33	(hierarchy hierarchical) with gated	USPAT	OR	OFF	2004/12/17 12:22
S66	6001	subcircuit sub-circuit	USPAT	OR	ON	2004/12/17 12:23
S67	13	(S50 S53) with S66	USPAT	OR	OFF	2004/12/17 12:27
S68	110486	(functional function circuit logic execution) adj (unit block)	USPAT	OR	ON	2004/12/17 12:27
S69	120	(S50 S53) with S68	USPAT	OR	OFF	2004/12/17 12:27
S70	142	(S50 S53) near6 portion	USPAT	OR	ON	2004/12/17 12:41
S71	14676	instruction near4 (fetch decode\$1 issue scheduler dispatch\$2)	USPAT	OR	OFF	2004/12/17 14:10
S72	1144	S71 with gat\$3	USPAT	OR	OFF	2004/12/17 14:11
S73	25	S71 with clock near2 gat\$3	USPAT	OR	OFF	2004/12/17 14:19
S74	10	S71 with clock near2 enable	USPAT	OR	OFF	2004/12/17 14:19
S75	79	("5392437").URPN.	USPAT	OR	OFF	2004/12/17 14:43
S76	4	S71 same (S50 S53)	USPAT	OR	OFF	2004/12/17 14:43
S77	10889	adder with multiplier	USPAT	OR	OFF	2004/12/17 15:02
S78	457	S77 same (fpu "floating point")	USPAT	OR	OFF	2004/12/17 15:10
S79	22	S78 same gat\$3	USPAT	OR	OFF	2004/12/17 15:06
S80	40	S78 same load adj store	USPAT	OR	OFF	2004/12/17 15:06
S81	351	S77 with (fpu "floating point")	USPAT	OR	OFF	2004/12/17 15:10
S82	44	S81.clm.	USPAT	OR	OFF	2004/12/17 15:19
S83	176	S68 with S66	USPAT	OR	OFF	2004/12/17 16:06
S84	23	S83 same clock\$3	USPAT	OR	OFF	2004/12/17 15:59
S85	104	S68 with (sub-unit subunit)	USPAT	OR	OFF	2004/12/17 16:09
S86	5	S85 same clock\$3	USPAT	OR	OFF	2004/12/17 16:07
S87	509	monitor\$3 adj3 stages	USPAT	OR	OFF	2004/12/17 16:09
S88	3	S87 with S68	USPAT	OR	OFF	2004/12/17 16:10
S89	1	S87 with (alu fpu "floating point")	USPAT	OR	OFF	2004/12/17 16:10
S90	2	alexander-michael-\$ and "international business".as.	USPAT	OR	OFF	2004/12/20 10:30
S91	213	"approximation circuit"	USPAT	OR	OFF	2004/12/20 10:55
S92	1	S91 same (fpu "floating point")	USPAT	OR	OFF	2004/12/20 11:17
S93	47	S91.clm.	USPAT	OR	OFF	2004/12/20 11:04
S94	0	S91 same pipeline	USPAT	OR	OFF	2004/12/20 11:01

S95	16	S91 same (divisor divid\$3)	USPAT	OR	OFF	2004/12/20 11:04
S96	13	("4797849").URPN.	USPAT	OR	OFF	2004/12/20 11:07
S97	2516	(divis\$3 divid\$3) same (fpu "floating point")	USPAT	OR	OFF	2004/12/20 11:18
S98	552	"square root" same (fpu "floating point")	USPAT	OR	OFF	2004/12/20 11:19
S99	194	(S97 S98) same cpu	USPAT	OR	OFF	2004/12/20 11:19
S10 0	0	S99.ab.	USPAT	OR	OFF	2004/12/20 11:18
S10 1	0	S99.clm.	USPAT	OR	OFF	2004/12/20 11:18
S10 2	3534	(pipeline pipe) adj stage	USPAT	OR	ON	2004/12/20 14:58
S10 3	11	S102 with clock near2 gat\$3	USPAT	OR	OFF	2004/12/20 15:12
S10 4	5	S102 with clock near2 enable	USPAT	OR	OFF	2004/12/20 15:14
S10 5	0	S102 with clock near3 zero	USPAT	OR	OFF	2004/12/20 15:14
S10 6	0	S102-same clock near3 zero	USPAT	OR	OFF	2004/12/20 15:15
S10 7	17	S102 with (inactivity activity)	USPAT	OR	OFF	2004/12/20 15:39
S10 8	111	S102 with (inactive active)	USPAT	OR	OFF	2004/12/20 15:43
S10 9	4	S108 same power	USPAT	OR	OFF	2004/12/20 15:18
S11 0	18	S108 same clock	USPAT	OR	OFF	2004/12/20 15:19
S11 1	36	S102 with monitor\$3	USPAT	OR	OFF	2004/12/20 15:22
S11 2	3638	fpu "floating point" adj3 unit	USPAT	OR	OFF	2004/12/20 15:22
S11 3	3	S112 with clock near2 gat\$3	USPAT	OR	OFF	2004/12/20 15:37
S11 4	5	S112 with clock near2 enabl\$3	USPAT	OR	OFF	2004/12/20 15:37
S11 5	34	S112 with (inactivity activity)	USPAT	OR	OFF	2004/12/20 15:39
S11 6	31	S112 with (inactive active)	USPAT	OR	OFF	2004/12/20 15:45
S11 7	81	S112 with monitor\$3	USPAT	OR	OFF	2004/12/20 17:05
S11 8	1948	selective\$2 adj power\$3	USPAT	OR	OFF	2004/12/20 18:24
S11 9	180	S118.ab.	USPAT	OR	OFF	2004/12/20 17:06
S12 0	7	("5696953").URPN.	USPAT	OR	OFF	2004/12/20 17:21
S12 1	10	("5586307").URPN.	USPAT	OR	OFF	2004/12/20 17:26
S12 2	3	("5200912").URPN.	USPAT	OR	OFF	2004/12/20 17:30
S12 4	239	selective\$2 adj clock\$3	USPAT	OR	OFF	2004/12/20 17:31
S12 5	11	S124.ab.	USPAT	OR	OFF	2004/12/20 17:32

S12 6	2	S118 same (fpu "floating point")	USPAT	OR	OFF	2004/12/20 17:32
S12 7	13	("6219796").URPN.	USPAT	OR	OFF	2004/12/20 17:38
S12 8	405	clock near2 (gat\$3 enabl3) with internal	USPAT	OR	OFF	2004/12/20 18:17
S12 9	53	S128.clm.	USPAT	OR	OFF	2004/12/20 18:17
S13 0	546	selectively adj (disabl\$3 enabl\$3) with clock\$1	USPAT	OR	OFF	2004/12/20 18:13
S13 1	30	S130.ab.	USPAT	OR	OFF	2004/12/20 18:15
S13 2	6	S130 with local	USPAT	OR	OFF	2004/12/20 18:16
S13 3	20	S130 with internal	USPAT	OR	OFF	2004/12/20 18:16
S13 4	173	S130.clm.	USPAT	OR	OFF	2004/12/20 18:17
S13 5	100	clock near2 (gat\$3 enabl3) with local	USPAT	OR	OFF	2004/12/20 18:23
S13 6	12	(S118 S124) with local	USPAT	OR	OFF	2004/12/20 18:23
S13 7	4633	selective\$2 adj2 gat\$3	USPAT	OR	OFF	2004/12/20 19:15
S13 8	396	S137 same power\$3	USPAT	OR	OFF	2004/12/20 18:32
S13 9	26	S138.ab.	USPAT	OR	OFF	2004/12/20 18:24
S14 0	79	S138.clm.	USPAT	OR	OFF	2004/12/20 18:28
S14 1	0	("6289465").URPN.	USPAT	OR	OFF	2004/12/20 18:28
S14 2	3	S137 same execution adj unit	USPAT	OR	ON	2004/12/20 18:29
S14 3	46	S137 same (functional circuit logic) adj (unit block)	USPAT	OR	ON	2004/12/20 18:30
S14 4	6	S137 same tree	USPAT	OR	ON	2004/12/20 18:31
S14 5	9	S137 same (fpu "floating point")	USPAT	OR	ON	2004/12/20 18:31
S14 6	423	S137 same clock\$3	USPAT	OR	OFF	2004/12/20 18:32
S14 7	248	S137 with clock\$3	USPAT	OR	OFF	2004/12/20 18:32
S14 8	23	S146.ab.	USPAT	OR	OFF	2004/12/20 18:32
S14 9	799	selective\$2 adj2 gat\$3	US-PGPUB	OR	OFF	2004/12/20 18:44
S15 0	26	S149 same clock\$3	US-PGPUB	OR	OFF	2004/12/20 18:47
S15 1	101	clock adj2 (tree distribution) with switch\$3	US-PGPUB	OR	OFF	2004/12/20 18:48
S15 2	48	clock adj2 (tree distribution) with switching	US-PGPUB	OR	OFF	2004/12/20 18:51
S15 3	99	clock adj2 (tree distribution) same switching	US-PGPUB	OR	OFF	2004/12/20 18:49

S15 4	86	clock adj2 (tree distribution) with switching	USPAT	OR	OFF	2004/12/20 19:14
S15 5	112	clock adj2 (tree distribution) with (consumption dissipation)	USPAT	OR	OFF	2004/12/20 19:11
S15 6	16	clock adj2 (tree distribution) with switching	EPO; JPO; IBM_TDB	OR	OFF	2004/12/20 19:10
S15 7	14	clock adj2 (tree distribution) with (consumption dissipation)	EPO; JPO; IBM_TDB	OR	OFF	2004/12/20 19:11
S15 8	14	clock adj2 (tree distribution) with efficiency	USPAT	OR	OFF	2004/12/20 19:14
S15 9	5	S137 with partial	USPAT	OR	OFF	2004/12/20 19:17
S16 0	267	S137 with portion	USPAT	OR	OFF	2004/12/20 19:17
S16 1	20	S160.ab.	USPAT	OR	OFF	2004/12/20 19:17
S16 2	188	partial adj2 tree	USPAT	OR	OFF	2004/12/20 19:18